

REMARKS

The rejection of claims 1-9 and 16 under 35 USC §102(e) as anticipated by Jimarez et al., is in error. Claims 1 and 7 require “a semiconductor chip mounted on a mounting substrate” and “a second resin filling a space between the semiconductor chip and the stiffener...the first resin being different in a thermal expansion coefficient from the second resin”. For the purpose of anticipating claims 1 and 7, the Examiner, on page 3 of the office action, cites element 36 of Jimarez et al. as a semiconductor chip. However, Jimarez et al. specifically identifies element 36 as a capacitor. See, for example, column 2, line 44. Jimarez et al. does identify element 34 as being an I/C chip; however element 34 does not contain a second resin filling a space between semiconductor chip or a stiffener. The Examiner is respectfully directed to column 2, lines 67 through 3, line 2 of Jimarez, in which there is an explicit description that the material 42, which is equated by the Examiner with a second resin of the present invention, does not extend over and into contact with the I/C chip (34). The I/C chip 34 of Jimarez is thus free from being covered with any resin. In other words, Jimarez neither teaches nor suggests such a resin that fills a space between the semiconductor chip and a stiffener in contact with the first resin as required. Since any resin is provided to cover the I/C chip 34, it is clear that Jimarez teaches away the relationship in thermal expansion coefficient between two resins as specified by claims 1 and 7.

Since this basic and essential feature of Applicant’s claimed invention as defined by independent claims 1 and 7 is not found anywhere within the four corners of Jimarez et al., and the advantages thereof are not recognized by Jimarez et al., it is submitted that the rejection of

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claims 1 and 7, and the several claims 2-6, 8, 9, and 16 which depend directly or indirectly on claim 1 or 7 cannot be said to be anticipated by or obvious from Jimarez et al..

The rejection of claims 1-4, 6-9, 16 and 17 under 35 USC §103(a) as being unpatentable over Caletka et al. also is in error. As noted supra, claims 1 and 7 require "the first resin being different in a thermal expansion coefficient from the second resin." The Examiner identifies element 17 of Caletka et al. as the first resin and element 26 as the second resin. However, Caletka et al. fails to disclose element 17 having a coefficient of thermal expansion ("CTE") that is different the CTE of element 26. Caletka et al. discusses CTE of some of the flip chip parts extensively, but never mentions the CTE of these resins or their relative values. The Examiner includes three paragraphs of the reference on pages 6 and 7 of the 12/15/06 office action, but these excerpts of Caletka et al. only discuss the CTE of the chip 12 and the laminate substrate 16. Thus, Caletka et al. fails to disclose or suggest all elements of the invention as claimed in claim 1 and claim 7 and the several claims 2-4, 6, 8, 9, 16, and 17, which depend, directly or indirectly therefrom, as the case may be.

Form PTO-2038 authorizing credit card payment in the amount of \$120.00 to cover the fee for Petition for Extension of Time accompanies this Amendment.

Having dealt with all the objections raised by the Examiner, the Application is believed to be in order for allowance. Early and favorable action is respectfully requested.

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In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account Number 08-1391.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 16, 2007, at Tucson, Arizona.

By

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